



Docket No.: 60188-124

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of :
Yasuyuki DOI, et al. :
Serial No.: Group Art Unit:
Filed: December 31, 2001 Examiner:
For: LIQUID CRYSTAL DRIVING CIRCUIT, SEMICONDUCTOR INTEGRATED
CIRCUIT DEVICE, REFERENCE VOLTAGE BUFFERING CIRCUIT, AND
METHOD FOR CONTROLLING THE SAME

INFORMATION DISCLOSURE STATEMENT

Honorable Commissioner for Patents and Trademarks
Washington, D. C. 20231

Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached form PTO-1449. It is respectfully requested that the references be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed with the application and no certification or fee is required.

A copy of the foreign search report is attached for the Examiner's information. Please note this is a PCT application in the entry of the National Phase in the U.S. and copies of the references cited were transmitted by WIPO and are believed to be in the file of the above identified application and readily available to the Examiner.

Serial No.:

Therefore it is believed that Applicants have met all requirements regarding duty of disclosure under 37 CFR 1.56. Acknowledgement and consideration of these documents are respectfully requested.

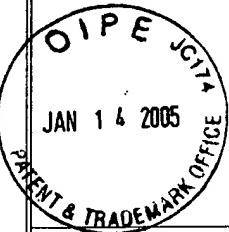
Respectfully submitted,

MCDERMOTT, WILL & EMERY



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INFORMATION DISCLOSURE CITATION IN AN APPLICATION  (PTO-1449)			ATTY. DOCKET NO. 60188-124	SERIAL NO.		
			APPLICANT Yasuyuki DOI, et al.			
			FILING DATE December 31, 2001	GROUP		
U.S. PATENT DOCUMENTS						
EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
	5,250,937	10/05/1993	KIKUO et al.			
	5,414,443	05/09/1995	KANATANI et al.			
	5,748,175	05/05/1998	SHIMADA et al.			
	5,751,279	05/12/1998	OKUMURA			
	5,754,150	05/19/1998	MATSUI			
	5,801,673	09/01/1998	SHIMADA et al.			
	6,002,384	12/14/1999	TAMAI et al.			
FOREIGN PATENT DOCUMENTS						
EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation
						Yes
	5-35220	02/12/1993	Japan (not enclosed)			
	5-273520	10/22/1993	Japan (not enclosed)			
	5-94159	04/16/1993	Japan (not enclosed)			
	7-191635	07/28/1995	Japan (not enclosed)			
	4-86787	03/19/1992	Japan (not enclosed)			
	3-165118	07/17/1991	Japan (w/ partial English translation)			
	62-154909	07/09/1987	Japan (w/ partial English translation)			
	63-45918	02/26/1988	Japan (w/ partial English translation)			
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)						
EXAMINER	DATE CONSIDERED					

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.



Docket No.: 60188-124

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of :
Yasuyuki DOI, et al. :
Serial No.: 10/019,437 : Group Art Unit: 2821
Filed: December 31, 2001 : Examiner: not yet assigned
:
For: LIQUID CRYSTAL DRIVING CIRCUIT, SEMICONDUCTOR INTEGRATED
CIRCUIT DEVICE, REFERENCE VOLTAGE BUFFERING CIRCUIT, AND METHOD
FOR CONTROLLING THE SAME(AS AMENDED)

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
Washington, DC 20231

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached form PTO-1449. It is respectfully requested that the references be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits. No certification or fee is required.

Each non-English language reference was first cited in the attached International Preliminary Examination Report and its relevance discussed therein.

10/019,437

Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

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INFORMATION DISCLOSURE CITATION IN AN APPLICATION			ATTY. DOCKET NO. 60188-124	SERIAL NO. 10/019,437			
(PTO-1449)			APPLICANT Yasuyuki DOI, et al.				
			FILING DATE December 31, 2001	GROUP 2821			
U.S. PATENT DOCUMENTS							
EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS		
FOREIGN PATENT DOCUMENTS							
EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						Yes	No
	9-44100	2/14/1997	Japan (w/ English Abstract)				
	59-154808	9/3/1984	Japan (w/ English Abstract)				
	9-244590	9/19/1997	Japan (w/ English Abstract)				
	5-204339	8/13/1993	Japan (w/ English Abstract)				
	2000-235376	8/29/2000	Japan (w/ English Abstract)				
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)							
EXAMINER	DATE CONSIDERED						

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.